

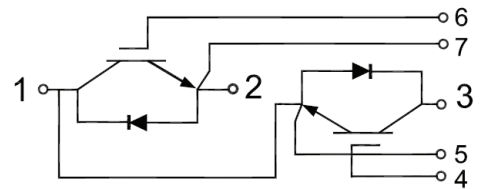
SYMT150HF170T2VH

IGBT Module

Preliminary Data

Features:

- Short Circuit Rated $>10\mu s$
- Low Saturation Voltage: $V_{CE(sat)} = 2.30V @ I_C = 150A, T_C=25^\circ C$
- Low Switching Loss
- 100% RBSOA Tested ($2 \times I_C$)
- Low Stray Inductance
- Lead Free, Compliant with RoHS Requirement



Applications:

- Industrial Inverters
- Motor Drives
- UPS Systems

IGBT, Inverter

Maximum Rated Values ($T_C=25^\circ C$ unless otherwise specified)

V_{CES}	Collector-Emitter Blocking Voltage		1700	V
V_{GES}	Gate-Emitter Voltage		± 20	V
I_C	Continuous Collector Current	$T_C = 80^\circ C$	150	A
		$T_C = 25^\circ C$	190	A
I_{CM}	Peak Collector Current Repetitive	$T_J = 150^\circ C$	300	A
t_{SC}	Short Circuit Withstand Time		>10	μs
P_D	Maximum Power Dissipation (IGBT)	$T_C = 25^\circ C$ $T_{Jmax}=150^\circ C$	880	W

Electrical Characteristics of IGBT($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Static Characteristics

Symbol	Description	Conditions	Min	Typ	Max	Unit
$V_{GE(th)}$	Gate-Emitter Threshold Voltage	$I_C = 1\text{ mA}, V_{CE} = V_{GE}$	5.5	6.0	6.5	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 150\text{A}, V_{GE} = 15\text{V}$		2.30	2.50	V
		$T_J = 125^{\circ}\text{C}$		2.70		V
I_{CES}	Collector-Emitter Leakage Current	$V_{GE} = 0\text{V}, V_{CE} = V_{CES}, T_J = 25^{\circ}\text{C}$			1	mA
I_{GES}	Gate-Emitter Leakage Current	$V_{GE} = \pm 20\text{V}, V_{CE} = 0\text{V}, T_J = 25^{\circ}\text{C}$			400	nA
C_{ies}	Input Capacitance	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$		10.2		nF
C_{oes}	Output Capacitance			0.54		nF

Switching Characteristics

$t_{d(on)}$	Turn-on Delay Time	$V_{CC} = 900V, I_C = 150A,$ $R_G = 10\Omega, V_{GE} = \pm 15V,$ Inductive Load	$T_J = 25^{\circ}C$		410		ns
			$T_J = 125^{\circ}C$		410		
t_r	Rise Time		$T_J = 25^{\circ}C$		150		ns
			$T_J = 125^{\circ}C$		150		
$t_{d(off)}$	Turn-off Delay Time		$T_J = 25^{\circ}C$		365		ns
			$T_J = 125^{\circ}C$		400		
t_f	Fall Time		$T_J = 25^{\circ}C$		250		ns
			$T_J = 125^{\circ}C$		370		
E_{on}	Turn-on Switching Loss		$T_J = 25^{\circ}C$		53.6		mJ
			$T_J = 125^{\circ}C$		63.3		
E_{off}	Turn-off Switching Loss		$T_J = 25^{\circ}C$		23.1		mJ
			$T_J = 125^{\circ}C$		38.0		
Q_g	Total Gate Charge		$T_J = 25^{\circ}C$		870		nC
RBSOA	RBSOA	$I_C=300A, V_{CC}=1650V, V_p=1700V,$ $R_g = 10\Omega, V_{GE}=+15V \text{ to } 0V, T_J = 150^{\circ}C$		Trapezoid			
SCSOA	SCSOA	$V_{CC} = 900V, V_{GE} = 15V,$ $T_J = 150^{\circ}C$		10		μs	
$R_{\theta JC}$	IGBT Thermal Resistance: Junction-To-Case				0.142	$^{\circ}C/W$	

Diode, Inverter

Maximum Rated Values($T_C=25^{\circ}\text{C}$ unless otherwise specified)

V_{RRM}	Repetitive Peak Reverse Voltage	1700	V
I_F	Diode Continuous Forward Current	150	A
I_{FM}	Peak FWD Current Repetitive	300	A

Electrical Characteristics of FWD($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Description	Conditions		Min	Typ	Max	Unit	
V _{FM}	Forward Voltage	I _F = 150A , V _{GE} = 0V	T _J = 25°C		1.70		V	
			T _J = 125°C		1.90			
I _{rr}	Peak Reverse Recovery Current	I _F = 150A, di/dt =1080A/μs, V _{rr} = 900V, V _{GE} = -15V	T _J = 25°C		95		A	
			T _J = 125°C		125			
Q _{rr}	Reverse Recovery Charge		T _J = 25°C		32.2		μC	
			T _J = 125°C		53.7			
E _{rec}	Reverse Recovery Energy		T _J = 25°C		17.6		mJ	
			T _J = 125°C		31.2			
R _{θJC}	Diode Thermal Resistance: Junction-To-Case				0.233		°C/W	

Module

Symbol	Description	Min	Typ	Max	Unit
V_{iso}	Isolation Voltage (All Terminals Shorted)	$f = 50\text{Hz}$, 1minute	2500		V
T_J	Maximum Junction Temperature			150	$^{\circ}\text{C}$
T_{JOP}	Maximum Operating Junction Temperature Range	-40 +150			$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-40 +125			$^{\circ}\text{C}$
$R_{\theta CS}$	Case-To-Sink Thermally (Conductive Grease Applied)		0.03		$^{\circ}\text{C}/\text{W}$
M	Power Terminals Screw:M6	3.0		5.0	N·m
M	Mounting Screw:M6	4.0		6.0	N·m
G	Weight		300		g

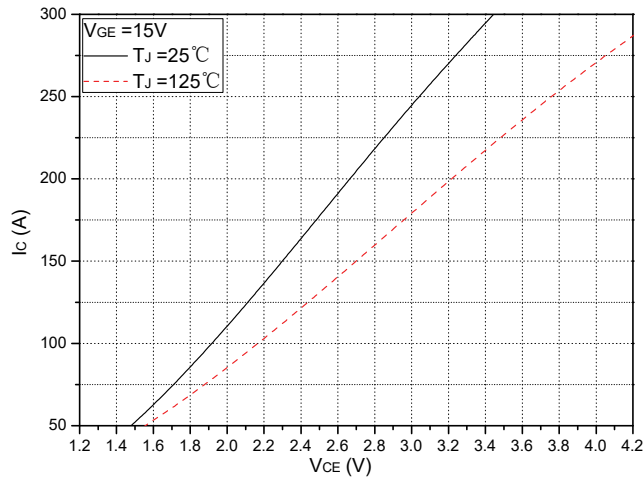


Fig.1 Typical Saturation Voltage Characteristics

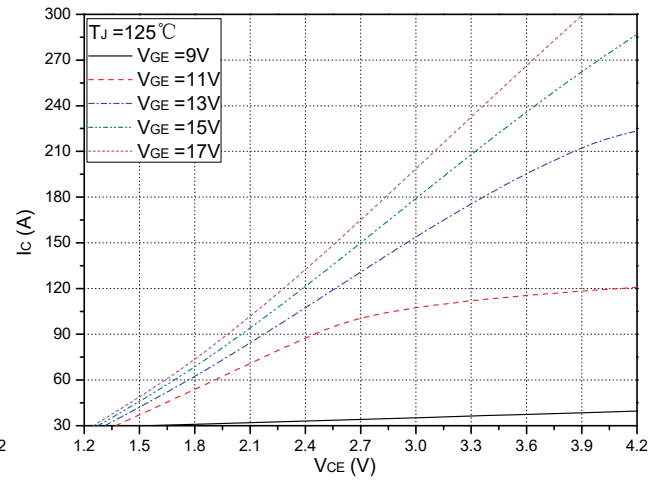


Fig.2 Typical Output Characteristics

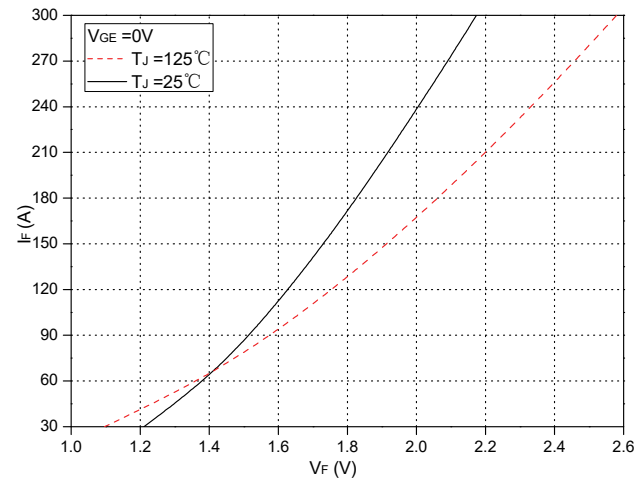


Fig.3 Forward Characteristics of FWD

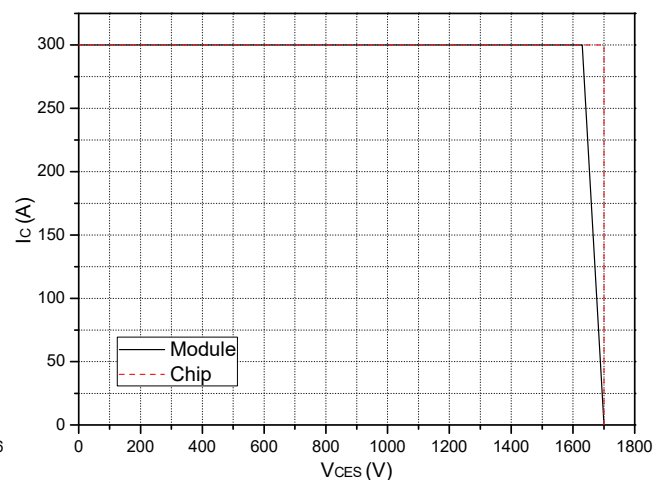


Fig.4 Reverse Bias Safe Operation Area (RBSOA)

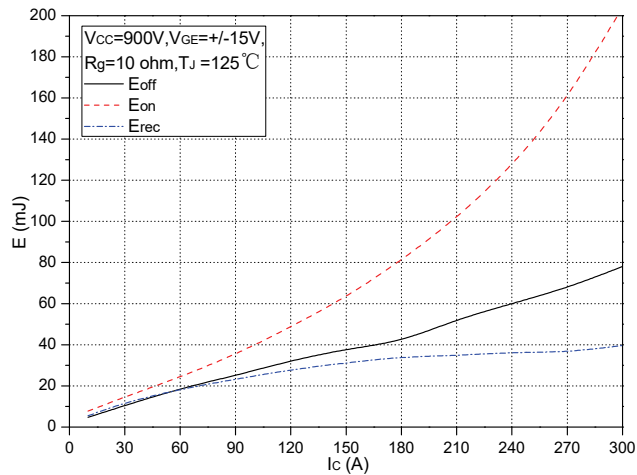


Fig.5 Typical Switching Loss vs. Collector Current

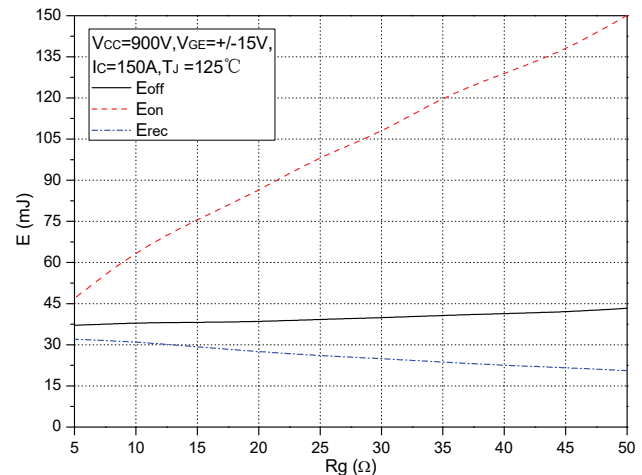


Fig.6 Typical Switching Loss vs. Gate Resistance

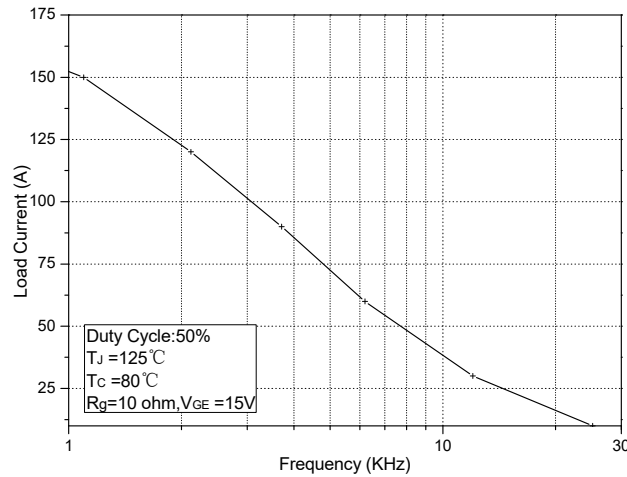


Fig.7 Typical Load Current vs. Frequency

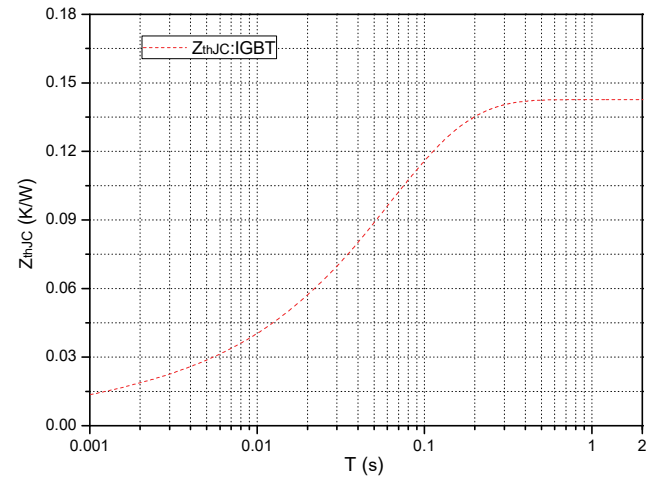


Fig.8 Transient Thermal Impedance (IGBT)

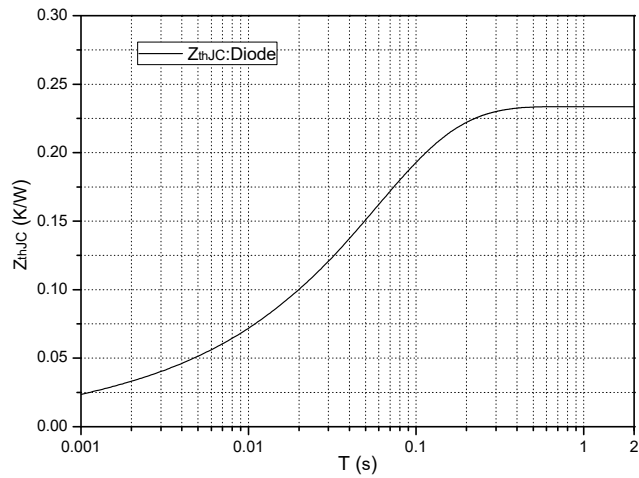


Fig.9 Transient Thermal Impedance (Diode)

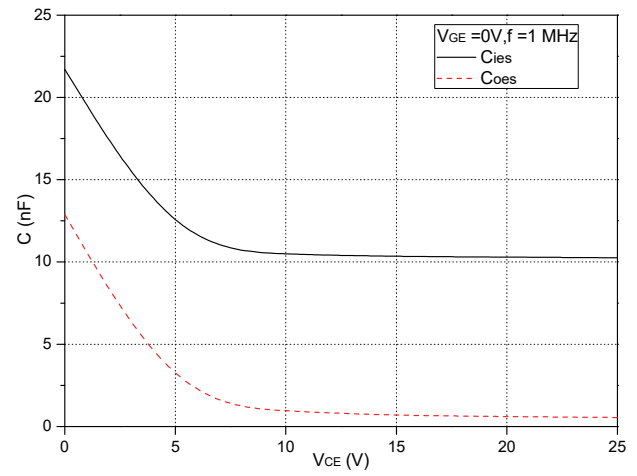


Fig.10 Capacitance Characteristics

